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Symbol-level iterative information set decoding of RS codes



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Abstract This paper presents the implementation of a low-complex iterative symbol-level decoding scheme for Reed-Solomon codes. Most soft-decision iterative decoders for Reed-Solomon codes work on a bit level due to the efficient passing of soft information due to the sparsity of the binary parity check matrix. These decoders yield a good error correction performance, but this comes at the cost of an increased computational complexity resulting from working at a bit-level. This study aims to lower the computational complexity of iterative decoding of Reed-Solomon codes by introducing a high-performance soft-decision iterative Reed-Solomon decoder that works on a symbol-level, in contrast to the bit-level implementation used in most iterative Reed-Solomon decoders. The proposed algorithm utilises soft information to decode while applying information set decoding techniques to an extended parity check matrix. The use of the extended parity check matrix provides additional parity check equations which assist the algorithm in determining the correct information set of symbols used in the decoding process. For a given (n, k) Reed-Solomon code, the algorithm converges to a valid codeword by correctly decoding a specified information set of k symbols from the received vector. Simulations run show the proposed algorithm performs favourably when compared to other symbol-level iterative decoders while working at a relatively lower complexity.

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1. Introduction

Forward error correction (FEC) is an error control strategy used in communication systems, deployed from the transmitter to the receiver. Error-correcting codes together with decoding algorithms are used to detect and correct errors that were introduced during transmission through a noisy communica-

tion channel. One of the most widely used error-correcting codes in FEC schemes are the Reed Solomon codes. Reed-Solomon (RS) codes are a class of non-binary Bose-Chaudhuri-Hocquenghem (BCH) codes that were developed in 1960 by Irving S. Reed and Gustave Solomon [1]. RS codes have played an important role in the telecommunications revolution with numerous applications in the Internet of Things (IoT), vehicular wireless communication technologies, digital storage devices, and satellite communication systems. Recently, RS encoding and transmission techniques have also been applied to optical communications [2,3].

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RS codes possess a convenient algebraic structure which enables the codes to detect and correct errors effectively, even in the presence of burst errors [4]. One of the key features of Reed-Solomon codes is their ability to correct errors that occur during transmission or storage. This is achieved by adding redundancy to the encoded data in the form of extra symbols called parity check symbols. These parity check symbols can be used to detect and correct errors that occur during transmission or storage. The popularity of RS codes has led to an active area of research focusing on improving their construction, developing efficient encoding techniques, and creating high-performance decoding schemes. [5–10,5].

Uncertainty methods are techniques that deal with the presence of noise, errors or ambiguity in data or models. They aim to quantify, propagate and estimate uncertainties in complex systems using probabilistic models, such as probability distributions or Bayesian inference. The most popular uncertainty method used in the iterative decoding of RS codes is the Adaptive Belief Propagation (ABP) algorithm [11]. This method is based on a soft-decision decoding approach that uses an iterative approach to update the likelihood of each possible codeword based on the received information. Uncertainty methods are not limited to FEC schemes and have applications in various fields [12–14].

The good error-correcting performance of the ABP when applied to RS codes is a direct result of the algorithm operating on a bit-level, which allows the decoder to make use of the sparse binary image of the matrix $\mathbf{H}$ [15,16]. As a result of this successful implementation, most iterative soft-decision RS decoders are in one way or another a variation of the ABP algorithm and, as a result, work on a bit-level [17–19,11,20]. These variants have been shown to output a favourable error-correcting performance when matched up to the original ABP decoder. However, this improved performance in error correction is realized at the cost of a higher computational complexity [17,11]. Decoding on a bit-level, which requires an $\mathbf{H}$ matrix that is comparatively larger than its symbol-level form, and applying Gaussian row reduction operations in each iteration, significantly adds to the increased cost in computational complexity of the ABP algorithm [11].

In an attempt to avoid the computational complexity cost of iteratively decoding soft information at a bit-level, research into the implementation of symbol-level decoders led to the development of the Parity-check Transformation Algorithm (PTA) [21]. The decoder works by reconstructing the matrix $\mathbf{H}$ based on the soft symbol reliability information of the RS code. The reliability information is then corrected based on the syndrome check equations in order to increase the likelihood of the correct symbol being selected. This process is repeated until the most likely decoded vector is obtained. The main novelty of the algorithm is that it can iteratively correct the soft information acquired from the received vector at the output of the channel based on the dense symbol-level $\mathbf{H}$ matrix. The decoding approach presented by the PTA is seen to exhibit better error-correcting performance when compared to the Koetter-Vardy (kV) [22] decoder and the Berlekamp Massey (BM) [23] decoding algorithm in [21,24]. However, it has to be noted that the decoder requires numerous iterations during the decoding process as a direct result of the dense composition of the $\mathbf{H}$ matrix [25]. The numerous iterations run by the PTA significantly contribute to its high cost in computational complexity.

Since it was first proposed by Prange [26], in all its forms, the Information Set Decoding (ISD) approach makes use of k linearly independent symbols or bits obtained at the channel output to re-encode a distinct codeword [27]. The k bits or symbols required for the re-encoding process are called the information set. Most implementations of the ISD approach used for error detection and correction in linear block codes have applications for binary codes. ISD algorithms mostly used in FEC also utilise list decoding techniques and Ordered Statistics Decoding (OSD) to correct the errors in the received vector [28–30]. One of the main advantages of using an ISD approach while decoding is that these algorithms have the benefit of a relatively lower complexity cost when compared to other decoding techniques [27] [31]. However, it has to be noted that ISD algorithms which utilise soft information are limited to soft-input hard-output decoders when implemented in conjunction with OSD and list decoding techniques. As a result, the implementation of these algorithms in circumstances where RS decoders are needed to fully utilise soft information derived from either the bits or symbols is restricted. This prohibits the use of ISD algorithms for applications that involve satellite transmission[32] or iterative decoding [33].

1.1. Motivation

Reed-Solomon codes can be decoded either on a bit-level or a symbol-level. Bit-level decoding algorithms operate on the binary representation of the symbols. Most iterative soft-input soft-output decoders for RS codes operate on a bit-level due to the sparse nature of the binary $\mathbf{H}$ matrix. In contrast, the dense structure of the symbol-level $\mathbf{H}$ matrix hinders efficient message passing of soft information during the decoding process. Bit-level soft-decision iterative decoders offer favourable error correction performance. However, this approach has several limitations, such as increased complexity, and loss of information. Therefore, this research focuses on an iterative symbol-level decoding approach that directly estimates the transmitted symbols from the received signals without converting them to bits. The main aim of implementing an iterative symbol-level decoder for RS codes is to reduce the decoding computational complexity while potentially improving the decoding accuracy and preserving the information content of the symbols.

The PTA is a soft-decision symbol-level iterative decoder for RS codes that yields a favourable error correction performance when compared to other RS decoders. However, due to the dense nature of the $\mathbf{H}$ matrix, the PTA requires numerous iterations during the decoding process. Due to the challenges presented by having a dense structure in the symbol-level $\mathbf{H}$ matrix, investigations into the implementation of information set decoding using iterative soft-decision techniques at the symbol-level has yet to be fully explored.

This paper presents an error correction technique that allows for iterative decoding using a soft-input soft-output approach on the information set of the RS code implemented on a dense symbol-level $\mathbf{H}$ matrix. The proposed decoding technique adds extensions to the dense $\mathbf{H}$ matrix [34] intended to add sparsity while still working at a symbol-level. The extensions to the parity check matrix are made up of low-weight parity check equations that are added to the original symbol-level $\mathbf{H}$ matrix. The weight of all the parity check equations,

added to the $\mathbf{H}$ matrix, matches the minimum distance [4] of the code. Each extension to the parity check matrix has similar dimensions as the original $\mathbf{H}$ matrix. This enables the columns from each extension of the parity check matrix to be arranged in such a way that they generate different information sets that are unique to the corresponding extensions. The use of different information sets in each extension, coupled with the added sparsity of the extended $\mathbf{H}$ matrix, allows for a more efficient exchange of the soft information at a symbol level while decoding the received vector. This results in the reduced number of iterations required during the decoding process, which in turn yields a lower computational complexity cost for the proposed algorithm. Results from the simulations show that the proposed decoder yields a favourable performance with regard to error correction, iterative convergence and computational complexity when compared to the other soft-decision symbol-level iterative RS decoders.

2. Information set decoding principles and the importance of the parity check matrix extensions

Prior to presenting the proposed decoder implementation, the significance of the systematic structure in the parity check matrix $\mathbf{H}$, which is necessary for the information set decoding approach, is discussed as follows:

Consider an (n, k) RS code $\mathcal{C}$ in the field $\text{GF}(2^b)$, where n is the length of the codeword, k is the length of the message and b is a positive integer. The RS code has a systematic $\mathbf{H}$ matrix with the form.

$$\mathbf{H} = [\mathbf{I}|\mathbf{Q}], \quad (1)$$

where $\mathbf{I}$ and $\mathbf{Q}$ denote the identity submatrix and parity submatrix respectively. The $\mathbf{I}$ submatrix has the dimensions $(n-k) \times (n-k)$, while the submatrix $\mathbf{Q}$ has the dimensions $(n-k) \times k$. The RS codeword $\mathbf{c}$ is then transmitted through a noisy channel to get the received vector, $\mathbf{r}$. The soft information obtained from the vector $\mathbf{r}$ is then used to get the hard-decision vector $\hat{\mathbf{c}}$, which is of the form given in (2).

$$\hat{\mathbf{c}} = [\hat{c}_1, \hat{c}_2, \dots, \hat{c}_n]. \quad (2)$$

The vector $\hat{\mathbf{c}}$ can be divided into two parts as shown in (3)

$$\begin{aligned} \mathbf{Y}_1 &= [\hat{c}_1, \hat{c}_2, \dots, \hat{c}_{(n-k)}], \\ \mathbf{Y}_2 &= [\hat{c}_{(n-k)+1}, \dots, \hat{c}_n]. \end{aligned} \quad (3)$$

$\mathbf{Y}_1$ has a length of $(n-k)$ and matches the indices of the submatrix $\mathbf{I}$ and $\mathbf{Y}_2$ has a length of k and matches the indices of submatrix $\mathbf{Q}$. Using the vectors $\mathbf{Y}_1$ and $\mathbf{Y}_2$, the equation for the syndrome $\mathbf{S}$ can be expanded from 4–5 by computing the product between $\hat{\mathbf{c}}$ and the transposed $\mathbf{H}$ matrix denoted as $\mathbf{H}^T$

$$\mathbf{S} = \hat{\mathbf{c}} \cdot \mathbf{H}^T, \quad (4)$$

$$\mathbf{S} = (\mathbf{Y}_1 \cdot \mathbf{I}^T) + (\mathbf{Y}_2 \cdot \mathbf{Q}^T). \quad (5)$$

Due to the identity matrix submatrix $\mathbf{I}$, (5) can be reduced to

$$\mathbf{S} = \mathbf{Y}_1 + (\mathbf{Y}_2 \cdot \mathbf{Q}^T). \quad (6)$$

Assuming $\hat{\mathbf{c}}$ is a valid codeword, then the syndrome $\mathbf{S} = \mathbf{0}$. Therefore, the additive inverse property of the field $\text{GF}(2^b)$ [4] can be applied in (6) to give

$$\mathbf{Y}_1 = \mathbf{Y}_2 \cdot \mathbf{Q}. \quad (7)$$

From (7) it can be seen that it is possible to decode the received vector of length n with an information set of k symbols, by utilising the systematic structure of $\mathbf{H}$. However, for the proposed decoding technique to successfully work, all the k symbols in $\mathbf{Y}_2$ have to be correct. This presents a challenge in the proposed decoding approach. This is because, for RS codes, the parity submatrix $\mathbf{Q}$ is completely dense. This means all the symbols in $\mathbf{Y}_2$ will participate in each syndrome check equation for each row of $\mathbf{H}$. This makes it difficult to detect and correct the symbols that are wrong in $\mathbf{Y}_2$ in the iterative decoding process. To increase the chances of a successful implementation of the information set decoding approach on the dense $\mathbf{H}$ matrix, parity check matrix extensions are included in the decoding process [34]. This allows for the creation of multiple information sets depending on which matrix extension is being utilised in the iterative decoding process. This increases the chances of finding the most likely information set to use in the decoding of the remaining $(n-k)$ symbols.

3. The proposed iterative symbol-level decoding scheme

3.1. Error correction and detection

Similar to Section 2, Consider an (n, k) RS codeword $\mathbf{c}$ in $\text{GF}(2^b)$. The vector $\mathbf{r}$ is obtained through the transmission of the codeword $\mathbf{c}$ in a noisy channel. For transmission through a BPSK modulation scheme, the reliability matrix (β) is constructed using the approach presented in [35,36]. Each of the noisy symbols of $\mathbf{r}$ is made up of b noisy bits as shown in (8).

$$\mathbf{r} = ((r_{0,0}, \dots, r_{0,b-1}), (r_{1,0}, \dots, r_{1,b-1}), \dots, (r_{n-1,0}, \dots, r_{n-1,b-1})). \quad (8)$$

For $0 \leq j \leq n$ and $1 \leq q \leq b$, the likelihood for each bit in $\mathbf{r}$ is calculated as [36]

$$P_{r_{j,q}}^1 = \frac{1}{(1 + \exp((4 \times u \times r_{(j,q)})/N_0))}, \quad (9)$$

where $N_0 = 1$ is the noise spectral density and $\pm u$ denotes the amplitude of the binary channel inputs. The notation $P_{r_{j,q}}^1$ represents the likelihood that the q^{th} noisy bit of the j^{th} symbols in the received vector is a 1. The likelihood that the q^{th} noisy bit of the j^{th} symbol in the received vector is a 0 is denoted as $P_{r_{j,q}}^0$ and is obtained using (10)

$$P_{r_{j,q}}^0 = 1 - P_{r_{j,q}}^1. \quad (10)$$

The reliability matrix β is created using the bit likelihoods as presented in (11)

$$\beta_{(\alpha^z, n)} = \prod_{q=1}^b P_{r_{j,q}}^{\alpha_q^z}. \quad (11)$$

where α_q^z is the q^{th} bit of the binary representation of the symbol $\alpha^z \in \text{GF}(2^b)$. The notation $z = -\infty$ or $0 \leq z \leq 2^b - 2$. Please note that each element in the field $\text{GF}(2^b)$ is represented by a row in the β matrix as illustrated in [21]. The β matrix is then fed into the decoder. The decoding process for the proposed algorithm can be summarised as follows:

1. *Determining the symbol reliabilities:* In this stage, the highest reliabilities from each column in β are stored in vector A . This is expressed as

$$A = [A_1, A_2, \dots, A_j] \quad (12)$$

where $A_j = \text{argmax}(\beta_j)$ for $0 \leq j \leq n$ and β_j denotes the individual columns of β . A sorting function is then applied to A to ensure the reliabilities are presented in ascending order. The indexes for the largest k reliabilities are then noted and placed in the vector $\mathcal{K}$. The vector $\mathcal{U}$ is used to store the remaining $(n - k)$ indexes which are considered to be unreliable. These vectors are shown in (13)

$$\begin{aligned} \mathcal{U} &= [l_1, l_2, \dots, l_{(n-k)}], \\ \mathcal{K} &= [l_{(n-k)+1}, \dots, l_n]. \end{aligned} \quad (13)$$

where l_j represents the symbol indexes.

2. *The H matrix extension:* The columns of matrix H are then rearranged using row reduction techniques to ensure that columns with the indexes $\mathcal{K}$ match the parity submatrix. A computationally efficient way of doing this can be seen in [37,38]. The indexes used to rearrange the columns for the corresponding H matrix extensions are obtained by using the equations shown in (14)–(16) [34] for $x = 1$

$$\begin{aligned} \mathcal{U}_{x=1} &= [l_1, l_2, \dots, l_{(n-k)}], \\ \mathcal{K}_{x=2} &= [l_{(n-k)+1}, \dots, l_n], \end{aligned} \quad (14)$$

for $x = 2$

$$\begin{aligned} \mathcal{U}_{x=2} &= [l_1, l_2, \dots, l_{((n-k)-(x-1))}, l_{(n-k)+1}], \\ \mathcal{K}_{x=2} &= [l_{((n-k)-(x-2))}, l_{(n-k)+2}, \dots, l_n], \end{aligned} \quad (15)$$

for $x \geq 3$

$$\begin{aligned} \mathcal{U}_{x \geq 3} &= [l_1, l_2, \dots, l_{((n-k)-(x-1))}, l_{((n-k)-(x-1))+1}, \dots, l_{(n-k)+1}], \\ \mathcal{K}_{x \geq 3} &= [l_{((n-k)-(x-2))}, l_{(n-k)+2}, \dots, l_n], \end{aligned} \quad (16)$$

where $1 \leq x \leq \varepsilon$ and ε represents the predefined number of parity check matrix extensions. The matrix extensions obtained from different index vectors are denoted as h_x . The complete extended parity check matrix is denoted as H_ε and is expressed as

$$H_\varepsilon = \begin{bmatrix} h_1 \\ \vdots \\ h_\varepsilon \end{bmatrix}. \quad (17)$$

3. *The syndrome check:* The hard-decision vector $\hat{c}$ is obtained from β and is used to get the syndrome, S , with the same approach shown in [21]. Each extension in H_ε has a syndrome vector denoted as s_x . The syndrome equation for each of the vectors s_x is calculated by getting the product between $\hat{c}$ and the corresponding transposed matrix extensions in H_ε as expressed in (18)

$$\begin{bmatrix} s_1 \\ \vdots \\ s_\varepsilon \end{bmatrix} = \hat{c} \times \begin{bmatrix} h_1^T \\ \vdots \\ h_\varepsilon^T \end{bmatrix}. \quad (18)$$

4. *Symbol voting ratios:* Voting for each symbol is performed during the syndrome calculation. All symbols, excluding the symbol being looked into, participate in the vote. The syndrome equation presented in (18) can also be expressed in the form of the scalar product between the $\hat{c}$ and each row of H_ε

$$s_{x_i} = \hat{c} \cdot h_{x_i}, \quad (19)$$

where $1 \leq i \leq (n - k)$ represents the row in each matrix extension of H . Due to the rearranged systematic configuration of the parity check matrix extensions of H_ε , only one symbol index of $\mathcal{U}_x$ participates in each row check. Therefore, (19) can be reduced to the form shown in (20)

$$s_{x_i} = \hat{c}_{t_i} \cdot h_{x_{(t_i)}}, \quad (20)$$

where t_i has a length of $k + 1$ and is used to denote the indices of the symbols in $\hat{c}$ participating in the i^{th} syndrome check equation of s_x . The indices denoted by the vector t_i can be written as

$$t_i = \{n : h_{x_{(t_i)}} \neq 0\}. \quad (21)$$

To apply the vote, it is assumed all the symbols except the one being voted for are correct. It is also presumed that $s_{x_i} = 0$. This means the additive inverse property for the $\text{GF}(2^b)$ field [4] is applied to (20) to give

$$\hat{c}_y \cdot h_{x_{(t_i)}} = \hat{c}_{t'} \cdot h_{x_{(t_i)}}, \quad (22)$$

where $y \in t_i$ denotes the index of the symbol that gets the vote. The subvector t' represents all the indexes in the vector t_i without the bit index y . To get the symbol vote for $\hat{c}_y$, (22) is rewritten as

$$\hat{c}_y = \frac{\hat{c}_{t'} \cdot h_{x_{(t_i)}}}{h_{x_{(t_i)}}}. \quad (23)$$

The actual value of $\hat{c}_y$ obtained from (23) is an element in the field $\text{GF}(2^b)$ and is counted as a single vote for the respective element. This is done for all the participating symbols in all the rows of H_ε . The votes are tallied for each element in the field and placed in V as shown

$$V = \begin{bmatrix} \alpha^{-\infty} & \alpha^0 & \alpha^1 & \dots & \alpha^{(2^b-2)} \\ V_{\alpha^{-\infty}} & V_{\alpha^0} & V_{\alpha^1} & \dots & V_{\alpha^{(2^b-2)}} \end{bmatrix}. \quad (24)$$

The first row of V represents each element in the field $\text{GF}(2^b)$. The second row of the matrix V has the values V_{α^z} , where z is either $-\infty$ or $0 \leq z \leq (2^b - 2)$, which are used to represent the total number of votes each of the corresponding elements gets. The vote ratios are then calculated as seen in (25) and denoted using η .

$$\eta = \left[\frac{V_{\alpha^{-\infty}}}{V_T}, \frac{V_{\alpha^0}}{V_T}, \frac{V_{\alpha^1}}{V_T}, \dots, \frac{V_{\alpha^{(2^b-2)}}}{V_T} \right], \quad (25)$$

where V_T represents the total number of votes cast and is expressed as

$$V_T = V_{\alpha^{-\infty}} + V_{\alpha^0} + \dots + V_{\alpha^{(2^b-2)}}. \quad (26)$$

5. *Obtaining the confidence rating:* The algorithm uses the confidence ratios as a measure of assurance that a symbol is correct based on the number of votes it gets. The confidence rating of each symbol is stored in the vector Γ and is calculated as

$$\Gamma = \frac{\eta}{\rho}, \quad (27)$$

where ρ represents a constant value defined in the initialisation stage of the decoding process. The confidence ratios for each symbol are represented as

$$\Gamma = [\Gamma_{\alpha^\infty}, \Gamma_{\alpha^0}, \dots, \Gamma_{\alpha^{(2^b-2)}]}. \quad (28)$$

6. *Updating β :* The confidence ratios are used to update the reliabilities in the matrix β . The update is based on the vote each symbol index gets in the voting stage. The update is performed by adding the confidence ratio of the symbol that wins the vote to its corresponding reliability in β . This update is performed on each of the participating symbols for every row of H_ε as shown.

$$\beta_{\widehat{c}_{y,j}}^{(f+1)} = \beta_{\widehat{c}_{y,j}}^{(f)} + \Gamma_{\widehat{c}_y}, \quad (29)$$

where f represents the current iteration number and $\widehat{c}_y$ is used to represent the symbol obtained from (23) as indexed in the matrix β . The matrix β is then scaled to ensure each column adds up to 1.

3.2. The stopping criteria

The steps for error detection and correction proposed in Section 3.1 are repeated until the stopping criteria are met. There are 2 stopping conditions defined for the proposed decoder. The main stopping condition is applied when the syndrome vector $S = 0$. The secondary stopping criteria is only applied when the algorithm meets the *decoding condition*. The decoding condition is defined as the moment when the algorithm determines that all the k symbols matching the parity submatrix of any of the matrices h_x are correct. Once the proposed symbol-level decoding algorithm deduces a correct information set of k symbols, the entire codeword can be re-encoded using (7).

The proposed decoder determines if the k information set symbols are correct by placing a threshold, τ , on the total number of syndrome check equations where $s_x = 0$ on a single parity check matrix extension h_x . When the number of satisfied syndrome check equations matches the value of τ during the decoding process, the proposed algorithm assumes the decoding condition has been achieved. For example, if $\tau = 4$, then only 4 elements in a single syndrome vector s_x should be equal to 0 for the decoding condition to be met. The remaining $(n - k)$ symbols matching the identity submatrix are then decoded using (7), based on the parity check matrix extension h_x that matches the corresponding syndrome vector s_x .

Meeting the decoding condition enables the proposed decoder to run fewer iterations. This is because the decoding condition helps the algorithm to quickly determine if the k information set symbols are correct as opposed to decoding the entire received codeword of length n . It has to be noted that the value of τ is used as a measure of confidence that the k symbols matching the parity submatrix are correct. Lower val-

ues of τ ensure that the decoding condition is met early, which guarantees fewer iterations while decoding. However, this comes at the cost of a reduction in the error correction performance due to the increased chances of errors in the information set.

4. Results and analysis

4.1. Investigating the optimal conditions for the proposed symbol-level decoding algorithm

In this section, an analysis is carried out on the proposed decoder to determine its optimum conditions linked to the different number of parity check matrix extensions. The findings from the analysis are quantified based on the Symbol Error Rate (SER) to measure the error correction performance, and the average number of iterations to measure the decoding iterative convergence performance. For ease of reference, the proposed decoder is called the k -Symbol Decoder and is denoted as kSD . Similar to Section 3, the number of parity check matrix extensions used with the kSD algorithm are denoted as ε .

A close to half rate (15, 7) RS code is used for the first set of tests. The variations of the kSD run in the simulations for this code include the decoder implemented with $\varepsilon = 2, \varepsilon = 3$ and $\varepsilon = 4$. All the variations of the kSD are run with $\rho = 20$ and $\tau = 3$. A BPSK modulation scheme is used to transmit the RS code through an AWGN channel. The simulation results are shown in Fig. 1 and Fig. 2.

The decoding performance exhibited by the kSD algorithm improves based on the number of extensions used, as seen in Fig. 1. The kSD with $\varepsilon = 4$ is the best-performing decoder out of the 3 algorithms. At an SER value of 10^{-4} , the kSD with $\varepsilon = 4$ yields slight gains of about 0.05 dB and 0.15 dB when compared to the kSD with $\varepsilon = 3$ and $\varepsilon = 2$ respectively. The gains of the kSD algorithm with $\varepsilon = 4$ are more visible in terms of the average iterative convergence performance required for decoding the received vector as seen in Fig. 2. The kSD with $\varepsilon = 4$, at its worst iterative convergence performance, uses an average of just about 7.5 iterations to detect and correct errors

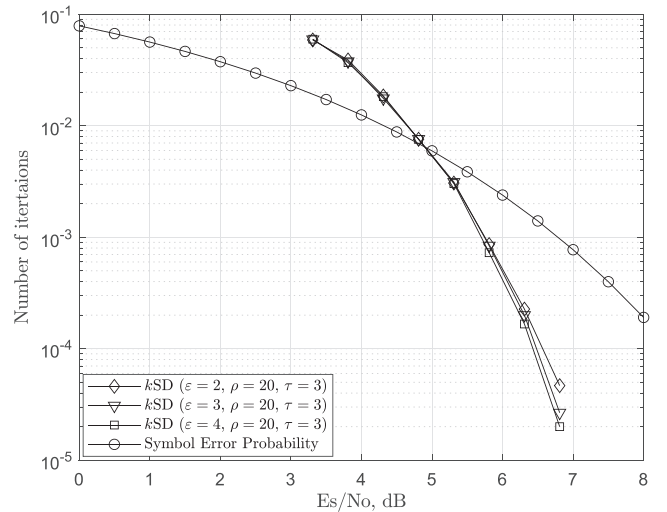


Fig. 1 SER performance comparison of the kSD for a (15, 7) RS code for different values of ε .

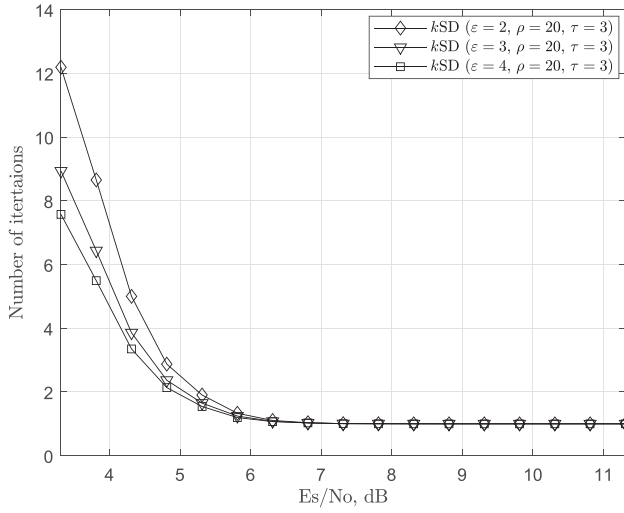


Fig. 2 Iterative convergence performance comparison of the kSD for a $(15, 7)$ RS code for different values of ε .

in the received vector. This is better than the kSD with $\varepsilon = 3$ and $\varepsilon = 2$ which, at their worst iterative convergence, run for an average of about 9 iterations and an average of just over 12 iterations respectively.

The proposed algorithm with $\varepsilon = 4$ exhibits relatively good performance in terms of SER and the average number of iterations because there are more combinations of symbol indexes of the vector $\mathcal{H}_x$ used with the different parity check matrix extensions h_x . This gives the algorithm more parity check equations which yield more syndrome vectors, s_x , that are used to check if the threshold is met. Using more syndrome vectors increases the chances of the decoding condition being met earlier, therefore decreasing the average number of iterations used in decoding the vector received at the output of the channel. It is also important to note that the larger size of H_ε when $\varepsilon = 4$ enables the algorithm to make more updates to the β matrix in a single iteration, which helps the algorithm converge to a codeword quicker. Due to the favourable iterative convergence performance exhibited by the kSD with $\varepsilon = 4, \rho = 20$ and $\tau = 3$, this version of the algorithm is used in the benchmarking tests for the nearly half rate case.

Similar tests are carried out on a high rate $(15, 11)$ RS code. Working with higher rates means implementing the proposed decoder on a H matrix that has fewer rows, resulting in a smaller length of each vector s_x obtained from the syndrome calculation in (18). Based on this, a smaller value of τ has to be used. For this set of simulations, the kSD is run with $\tau = 2$. The proposed algorithm is run under the same conditions with the same variants of the kSD algorithm as the nearly half rate case. The simulation results can be seen in Fig. 3 and Fig. 4.

Similar to the nearly half rate case, the proposed algorithm with $\varepsilon = 4$ is seen to yield a slightly better SER performance gain when matched up to the kSD with $\varepsilon = 3$ and $\varepsilon = 2$ as seen in Fig. 3. The kSD gives a coding gain of about 0.04 dB when compared to the proposed decoder with the $\varepsilon = 3$ and $\varepsilon = 2$ at an SER of 10^{-3} . Similar to the nearly half rate case, the true gains of using the kSD with $\varepsilon = 4$ are seen in terms of the iterative convergence performance. The kSD with $\varepsilon = 4$, at its worst iterative convergence performance, is seen in Fig. 4 to run for an average number of just under 18 iterations. This

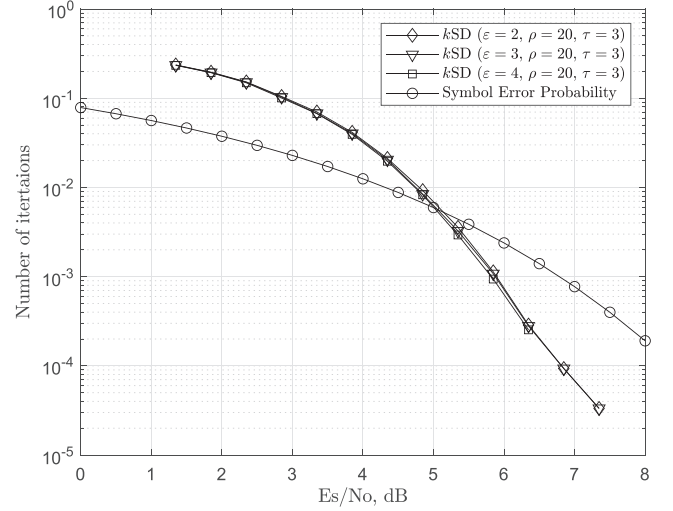


Fig. 3 SER performance comparison of the kSD for a $(15, 11)$ RS code for different values of ε .

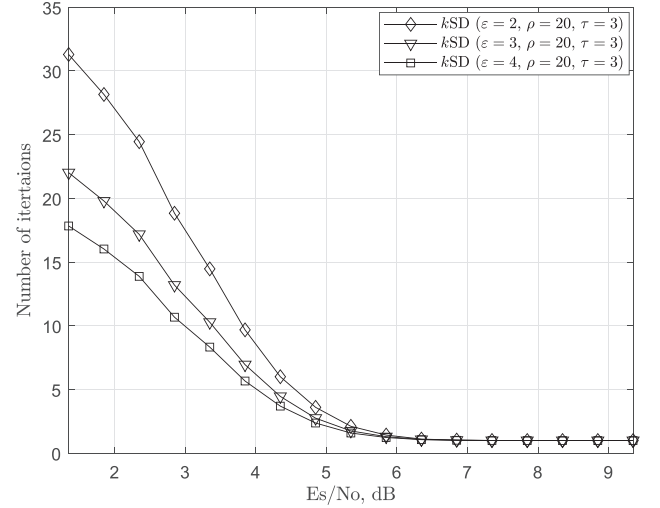


Fig. 4 Iterative convergence performance comparison of the kSD for a $(15, 11)$ RS code for different values of ε .

iterative convergence performance is better than the kSD with $\varepsilon = 3$ and $\varepsilon = 2$, which run for an average number of just over 22 and just over 28 iterations at their worst iterative convergence respectively. Based on the iterative convergence results, the kSD with $\varepsilon = 4$ is used in the benchmarking simulations for the high rate codes.

4.2. Performance analysis of the kSD Algorithm

In this section, an analysis into the performance of the proposed decoder is carried out when matched up against other iterative symbol-level decoders. The symbol-level implementations for the decoding algorithms are used in these simulations for the purpose of giving a fair comparison. The decoders used in the simulations are the symbol-level ABP algorithm and the PTA decoder.

4.2.1. Half rate codes

For these simulations the symbol-level ABP decoder is run with the damping factor, θ , having a value of 0.05 with the total number of iterations limited to a maximum of 20 at the initialisation of the algorithm [17]. While the PTA runs with a value of $\delta = 0.001$ [21]. The simulations are run under similar conditions as the previous tests for the k SD algorithm. The simulation results are presented in Fig. 5 and Fig. 6.

From Fig. 5, the k SD with $\varepsilon = 4$ is seen to be slightly outperformed by less than 0.05 dB when the error detection and correction performance is compared to that of the PTA decoder at an SER value of 10^{-3} . The k SD algorithm, when matched up to the symbol-level ABP algorithm, can yield a gain of about 0.5 dB at an SER value of 10^{-4} . From Fig. 6, the PTA is shown to run for numerous iterations while decoding the received vector, especially for the lower SNR regions. The PTA, at its worst iterative convergence performance, requires an average of about 643 iterations. This is a large number of iterations, especially when compared to the other iterative symbol-level decoders. The symbol-level ABP runs for just over 12 iterations. The k SD with $\varepsilon = 4$ can yield a better iterative convergence performance than all the algorithms, requiring an average maximum value of just about 7.5 iterations at its worst iterative convergence performance for the decoding process.

4.2.2. High rate codes

A similar investigation on the performance of the k SD is carried out on a high rate (15, 11) RS code. Simulation conditions used for the decoding algorithms remain the same as those considered for the close to half rate (15, 7) RS code and are displayed in Fig. 7 and Fig. 8.

The k SD with $\varepsilon = 4$ is seen to match the error correction performance of the symbol-level ABP in Fig. 7. However, when compared to the PTA, the k SD with $\varepsilon = 4$ gives a gain of 0.3 dB at the SER value of 10^{-3} . Similarly to the case of the (15, 7) RS codes, the PTA algorithm requires the most average number of iterations to complete the error detection and correction process of the received vector. At its worst iter-

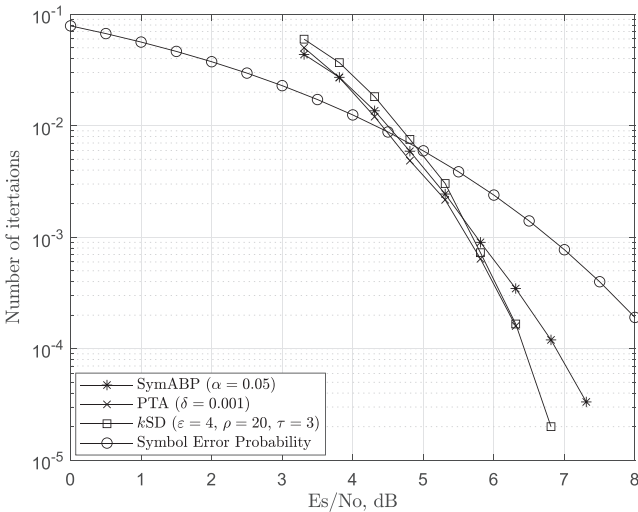


Fig. 5 SER performance comparison of the Symbol level ABP, PTA and the k SD for a (15, 7) RS code.

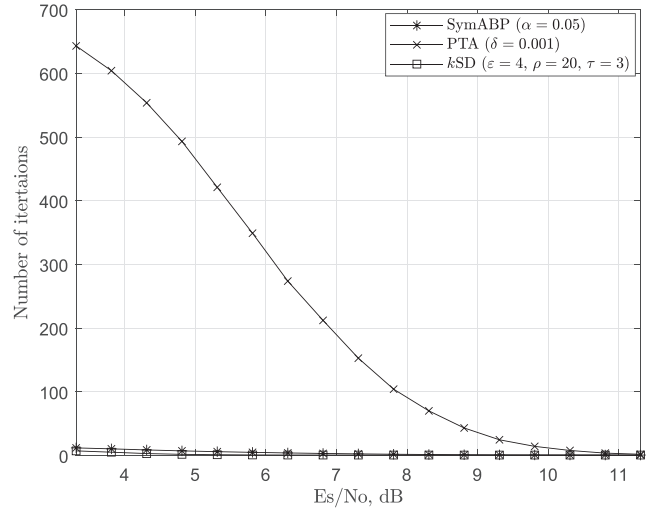


Fig. 6 Iterative convergence performance comparison of the Symbol level ABP, PTA and the k SD for a (15, 7) RS code.

ative convergence performance, the PTA runs for an average of about 775.8 iterations to complete the decoding process as seen in Fig. 8. The symbol-level ABP requires an average of about 19.2 iterations to converge to a codeword at its worst iterative convergence performance. The k SD with $\varepsilon = 4$ requires the least number of iterations for the detection and correction of errors in the received vector. At its worst iterative convergence performance, the k SD with $\varepsilon = 4$ uses an average of about 17.8 iterations to complete the decoding process.

5. Complexity analysis

In this section, an investigation is carried out to analyse the complexity cost of the proposed decoder. The analysis also compared the complexity cost to that of the PTA and the Symbol-level ABP. The complexity cost of the decoding algorithms is measured in terms of time complexity together with the number of additions/subtractions (+/-) and multiplications/divisions ($\times/\div$). The notation for the computational calculations are represented as follows; the total number of rows in H are represented using the notation m . For the Symbol-level ABP, the average row weight of the participating symbols for the binary image of H classified by t are represented as W_t . The notation W_c is used to symbolise the average column weight of the binary image of H . The dimensions of the binary image of H are denoted as $mb \times nb$, where $mb = m \times b$ and $nb = n \times b$. The complexity of the decoding algorithms is summarised in Table 1.

From Table 1, the k SD is seen to have a complexity that is directly controlled by the number of parity check matrix extensions. The time complexity of the algorithm is also dominated by multiplications/divisions. However, the algorithm has much smaller dimensions when compared to the dimensions of the Symbol-level ABP which iteratively decodes the symbols based on the larger binary image of H . Also, the use of fewer iterations while decoding the received vector enables the k SD to operate at a relatively lower computational complexity. The PTA appears to be the least complex algorithm of all the three iterative decoders, however, the large number of iterations run

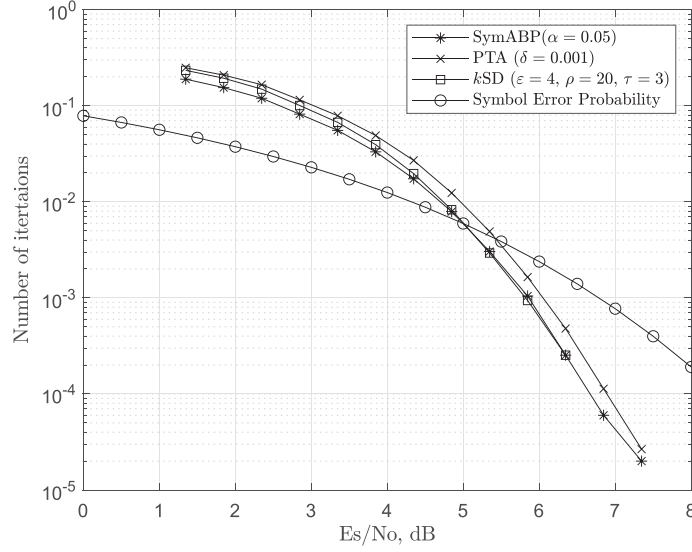


Fig. 7 SER performance comparison of the Symbol level ABP, PTA and the k SD for a (15, 11) RS code.

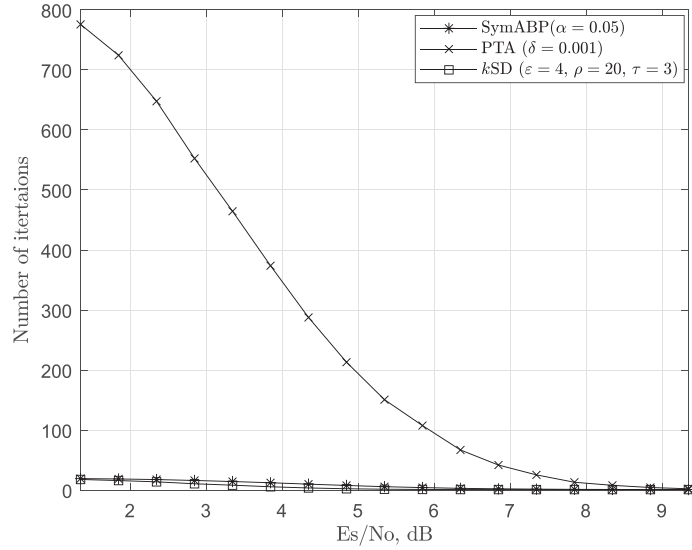


Fig. 8 Iterative convergence performance comparison of the Symbol level ABP, PTA and the k SD for a (15, 11) RS code.

Table 1 Summary of the complexity analysis for the symbol-level iterative decoders.

Decoder	Most complex stage	Time Complexity	+/-	$\times/\div$
SymABP	Obtaining extrinsic information	$\mathcal{O}(mb \times W_t^2)$ [17]	$(nb \times W_c)$	$(mb \times W_t^2)$
PTA	Transforming matrix H	$\mathcal{O}(m^2 \times n)$	$(m^2 \times n)$ [21]	$(m^2 \times n)$
k SD	Calculating symbol votes	$\mathcal{O}((m \times \varepsilon) \times k^2)$		$((m \times \varepsilon) \times (t + t^2))$

by the decoder while detecting and correcting errors in the received vector significantly adds to the overall cost in computational complexity.

6. Conclusion

A relatively low complexity iterative symbol-level soft-decision RS decoder is proposed in this paper. The algorithm has

several variations depending on the number of parity check matrix extensions used. The extensions to the parity check matrix add to the number of parity check equations used in the decoding process of the k SD. Rearranging the columns of the different parity check matrix extensions in line with the symbol reliability information, help in identifying errors based on the syndrome vectors obtained from the parity check equations. Using the parity check matrix extensions and placing a threshold on the total number of satisfied syndrome

check equations by the k symbols in the information set, improves the iterative convergence performance of the decoder. This in turn makes the decoder more efficient. Adding more parity check matrix extensions increases the complexity of the proposed decoder for a single iteration. However, the overall SER and iterative convergence performance is improved. This provides a justifiable tradeoff when selecting the number of parity check matrix extensions required during the decoding process.

The paper also shows the implementation of information set decoding techniques on a symbol-level. The proposed algorithm can converge to a valid codeword by correctly decoding a specified information set of k symbols from the received vector. This is especially significant for symbol-level iterative decoding schemes because of the dense nature of the symbol-level H matrix for RS code. The information set decoding implementation helps to significantly reduce the number of iterations required for decoding which in turn helps reduce the overall complexity of the decoder.

The proposed decoder performs favourably when compared with the existing symbol-level RS decoders. In comparison with the PTA decoder, the proposed algorithm with $\varepsilon = 4$ matches the performance for the close to half rate (15, 7) RS code. This performance is carried over to higher rate codes as the same version of the decoder yields similar performance to the symbol-level ABP for the (15, 11) RS code. However, it has to be noted that the error correction performance exhibited by the Symbol-level ABP and the PTA comes at the expense of the decoders operating at a higher computational complexity cost than the k SD with $\varepsilon = 4$. This gives justification for the use of the k SD in situations where a performance-complexity tradeoff is necessary.

For future works, several other potential techniques can be investigated in the selection of the index vectors used to rearrange the different parity check matrix extensions h_x . This would help in the selection of a more diverse set of vectors used in obtaining the parity check matrix extensions. This will help to improve the performance and efficiency of the proposed algorithm without a significant increase in complexity.

Declaration of Competing Interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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